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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/656,333	09/08/2003	Daiji Kitagawa	83993-000002/US	9215
30593	7590	05/17/2006	EXAMINER	
HARNESS, DICKEY & PIERCE, P.L.C. P.O. BOX 8910 RESTON, VA 20195				WU, XIAO MIN
ART UNIT		PAPER NUMBER		
		2629		

DATE MAILED: 05/17/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/656,333	KITAGAWA ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	XIAO M. WU	2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### **Status**

- 1) Responsive to communication(s) filed on 08 September 2003.
- 2a) This action is **FINAL**.                            2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### **Disposition of Claims**

- 4) Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) 1-4 and 9-12 is/are rejected.
- 7) Claim(s) 5-8 and 13-20 is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### **Application Papers**

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 08 September 2003 is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### **Priority under 35 U.S.C. § 119**

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All    b) Some \* c) None of:
  1. Certified copies of the priority documents have been received.
  2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### **Attachment(s)**

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 9/8/2003.
- 4) Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) Notice of Informal Patent Application (PTO-152)
- 6) Other: \_\_\_\_\_.

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-4 and 9-12 are rejected under 35 U.S.C. 102(e) as being anticipated by Kudo (US Patent No. 6,753,880)

As to claims 1, 9, 11, Kudo discloses a display device which includes a display portion having a capacitive load (see element 121 of Fig. 12) and an output buffer (502, Figs. 5 and 6) having a driving capability that depends on a bias current (col. 9, lines 61-62), and which displays an image on the display portion by letting the output buffer apply an analog voltage (V0 to V63, Fig. 5) corresponding to an input image signal (115, Fig. 5) to the capacitive load to drive the display portion (121, Fig. 12), the display device comprising: a bias current control portion (SW1-SW8, Fig. 6, also see col. 9, line 49 to col. 10, line 3) that controls the bias current; wherein the output buffer (502) is configured such that the bias current can be dynamically changed (col. 9, lines 61-62); and wherein the bias current control portion changes the bias current while the display portion is driven (col. 10, lines 52-56).

As to claim 2, Kudo discloses the output buffer (502, Fig. 6) comprises: a plurality of transistors (MP5-MP8, MN4-MN7, Fig. 6), connected in parallel, for outputting the analog voltage (Vout); and a switching circuit (SW1-SW8, Fig. 6) for switching at least one of the

plurality of transistors between an operative state and an inoperative state; wherein the bias current control portion (SW1-SW8, Fig. 6) changes the bias current by changing the number of said plurality of transistors that are in the operative state with the switching circuit (see col. 9, lines 25 to col. 10, line 3).

As to claim 3, Kudo discloses the output buffer comprises: a transistor for outputting the analog voltage; and an operating point changing circuit for changing an operating point of the transistor (e.g. on or off of the transistor); wherein the bias current control portion changes the bias current by changing the operation point of the transistor with the operating point changing circuit (see col. 9, lines 25 to col. 10, line 3)..

As to claims 4, 10, 12, Kudo discloses the bias current control portion changes the bias current during a charge period or a discharge period, which is a period during which the output buffer is to apply the analog voltage to the capacitive load (see col. 16, line 35 to col. 17, line 9).

#### *Allowable Subject Matter*

3. Claims 5-8 and 13-20 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

4. The following is a statement of reasons for the indication of allowable subject matter:

None of the prior art references, alone or in combination, fairly suggests or discloses the limitation of “the bias current control portion controls the bias current such that, after a predetermined time within the charge period or the discharge period, the bias current is smaller than at the beginning of the charge period or the discharge period” as recited in claims 5 and 13.

None of the prior art references, alone or in combination, fairly suggests or discloses the limitation of “the bias current control portion determines, based on the input image signal, a time within the charge period or the discharge period at which the bias current is to be reduced, and controls the bias current such that, after that determined time, the bias current is smaller than at the beginning of the charge period or the discharge period” as recited in claims 6 and 14.

None of the prior art references, alone or in combination, fairly suggests or discloses the limitation of “the bias current control portion determines, based on a charge/discharge current flowing between the output buffer and the capacitive load, a time within the charge period or the discharge period at which the bias current is to be reduced, and controls the bias current such that, after that determined time, the bias current is smaller than at the beginning of the charge period or the discharge period” as recited in claims 7 and 15.

### ***Conclusion***

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

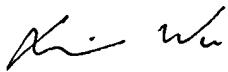
The US Patents 6,653,900 and 6,677,923 are cited to teach a flat panel display device including output buffer.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to XIAO M. WU whose telephone number is 571-272-7761. The examiner can normally be reached on 6:30 am to 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, RICHARD HJERPE, can be reached on 571-272-7691. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

X.W  
May 12, 2006

  
XIAO M. WU  
Primary Examiner  
Art Unit 2629